

D'Vend

interposed between the upper and lower substrates. The lower substrate 1 has a thin film transistor "S" (TFT) as a switching element and a pixel electrode 14. The upper substrate 3 has a color filter 8 and a common electrode 12. The pixel electrode 14 is formed over a pixel region "P" and serves to apply a voltage to the liquid crystal layer 10 along with the common electrode 12, and the color filter 8 serves to implement natural colors. A sealant 6 seals edges of the lower and upper substrates 1 and 3 to prevent a leakage of the liquid crystal.

IN THE CLAIMS

Please amend the claims as follows (A marked-up version of the amended claims is attached):

- 1. E*
1. A method of manufacturing a thin film transistor for use in an LCD device, comprising:
preparing a substrate and a mixed solution, the mixed solution having a reductant and a first metal;
forming a photoresist pattern on the substrate;
etching a portion of the substrate to form a groove beneath a top surface of the substrate using the photoresist pattern as a mask;
depositing a second metal on the substrate, a height of the second metal being smaller than a depth of the groove;
removing the photoresist pattern on the substrate and the second metal on the photoresist other than in the groove; and
forming the first metal on the second metal in the groove by submerging the substrate in the mixed solution.
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